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IR-1888 (2-2335)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Joseph MAGGIOLINO

Date : July 1, 2003

Serial No. : 09/553,756

Group Art Unit : 2816

Filed : April 21, 2000

Examiner : A. Tra

For : CIRCUITRY FOR HIGH VOLTAGE LINEAR CURRENT SENSE IC

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate

JUL 09 2003

of Correction

SUBMISSION OF CERTIFICATE OF CORRECTION

Sir:

In a Rule 312 amendment filed November 19, 2001 (enclosed), we explained that allowed claim 1 contained an error. Specifically, the specification did not support the final paragraph of allowed claim 1, namely "a recovery circuit for reconstructing the analog input signal at the second voltage level." The Rule 312 amendment deleted this paragraph.

We received no response to the Rule 312 amendment, and the patent issued with the erroneous paragraph still in claim 1, at lines 13-14.

Enclosed is a copy of a stamped receipt postcard which proves that the PTO received the Rule 312 amendment on November 19, 2001. The issue fee was paid and received by the PTO on November 20, 2001.

Therefore, we submit the enclosed Certification of Correction. As shown therein, claim 1 is being corrected by deleting lines 13-14 of claim 1 as issued. Entry is requested.

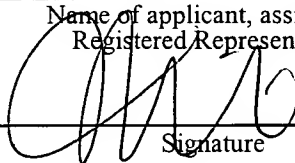
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Since a Rule 312 amendment was received by the PTO, but not processed, it is believed no fees are due. However, if the Patent Office deems any fees to be due, they may be charged to our Deposit Account No. 15-0700.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 1, 2003:

James A. Finder

Name of applicant, assignee or
Registered Representative



Signature

July 1, 2003

Date of Signature

Respectfully submitted,



James A. Finder

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Enclosures

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : **US 6,337,591 B1**
DATED : **January 8, 2002**
INVENTOR(S) : **Joseph Maggolino**

It is certified that error appears in the above identified patent and that said Letters Patent is hereby corrected as shown below.

[Handwritten: NO]
Claim 1
-- A current sense integrated circuit, comprising:
an amplifier circuit for receiving and amplifying a differential analog input signal at a first voltage level containing current sense information, wherein the amplifier circuit includes a circuit to minimize inherent temperature offset drift;
a pulse width modulator circuit for converting the differential analog input signal to a pulse width modulated signal at the first voltage level; and
a level shift circuit for converting the pulse width modulated signal from the first voltage level to a second voltage level.--

[Handwritten signature]

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PATENT NO. **6,337,591**

Our Docket:
IR-1888 (2-3335)



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